## **UNIVERSITY OF KERALA**

## Timetable for Third Semester B.Tech Degree Examination(Part time), Restructured December 2015/January 2016

(Parts subjects coming under Fifth semester 2008 scheme course codes)

| Day      | Date       | Time                 | Part time code                           | Course code                      | Subject   |
|----------|------------|----------------------|--|----------------------------------|---|
| Thursday | 17/12/2015 | 1.30 pm<br>to 4:30pm | PT10301<br>PT10303<br>PT10301            | 08501<br>08501<br>08501          | Engineering Mathematics IV(CM) Engineering Mathematics IV (ER) Engineering Mathematics IV – Complex Analysis & Linear Algebra (T) |
| Thursday | 31/12/2015 | 1.30 pm<br>to 4:30pm | PT10302<br>PT10304                       | 08502<br>08502                   | Electrical Technology (M) Advanced Mathematics and Queuing Models (R)   |
| Tuesday  | 05/01/2016 | 1.30 pm<br>to 4:30pm | PT10302<br>PT10304                       | 08503<br>08503                   | Structural Analysis II (C)<br>Computer Organization & Architecture<br>(T)   |
| Thursday | 07/01/2016 | 1.30 pm<br>to 4:30pm | PT10305<br>PT10305<br>PT10304<br>PT10305 | 08504<br>08504<br>08504<br>08504 | Transportation Engineering I (C) Industrial Electronics (M) Power Electronics (E) Electrical Drives & Control (T)                 |
| Thursday | 14/01/2016 | 1.30 pm<br>to 4:30pm | PT10305<br>PT10305                       | 08506<br>08506                   | Elective I (E) Object Oriented Design and JAVA Programming (R)  |

University Buildings, Thiruvananthapuram

**CONTROLLER OF EXAMINATIONS**