UNIVERSITY OF KERALA

Timetable for Fifth Semester B.Tech Degree Examination, (Part time) Restructured December 2015/January 2016 2008 Scheme

Day	Date	Time	Part time code	Course code	Subjects
Thursday	31/12/2015	1.30 pm to 4:30pm	PT10501 PT10501 PT10501	08502 08502 08502	Design of Reinforced Concrete Structures (C) Electronic Instrumentation (E) Digital Signal Processing (T)
Tuesday	05/01/2016	1.30 pm to 4:30 pm	PT10501 PT10502 PT10501	08503 08503 08503	Theory of Machines (MP) Electrical Measurements II (E) Data Base Design (R)
Thursday	07/01/2016	1.30 pm to 4:30 pm	PT10502	08504	Systems Programming (R)
Tuesday	12/01/2016	1.30 pm to 4:30 pm	PT10503 PT10502 PT10503 PT10502 PT10503	08505 08505 08505 08505 08505	Urban Planning and Architecture (C) Machine Tools (M) Electrical Machines II (E) Applied Electromagnetic Theory (T) Microprocessors and Interfacing (R)
Thursday	14/01/2016	1.30 pm to 4:30 pm	PT10504 PT10503 PT10503	08506 08506 08506	Elective I (C) Elective I (M) Elective I (T)

University Buildings, Thiruvananthapuram

CONTROLLER OF EXAMINATIONS